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FIG. 1

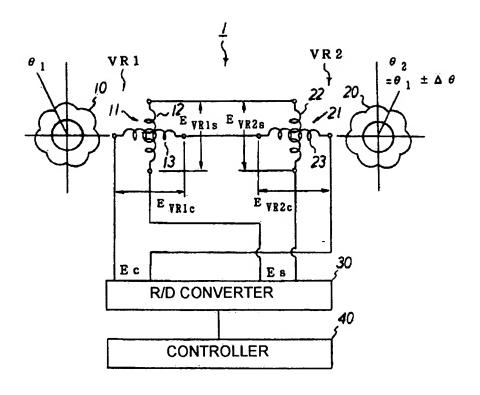
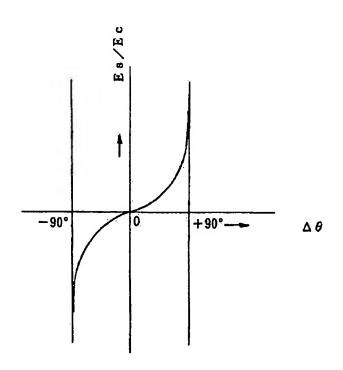
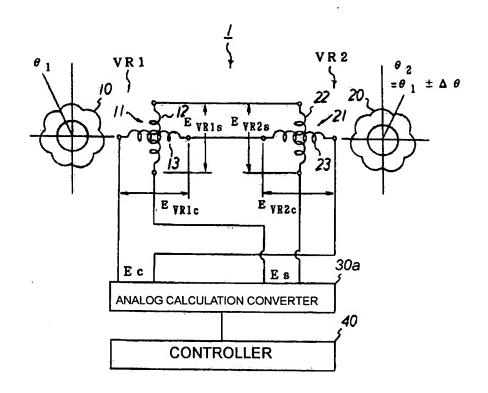


FIG. 2



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FIG. 3



3/3 FIG. 4 PRIOR ART 100 140 CONTROLLER 130 130 VR2  $\theta_2$ R/D CONVERTER VR1 R/D CONVERTER E c E c E s <sub>5</sub> 122

